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Investigation of the electrical properties of metal chalcogenide thin films: A review

S. M. HO

Faculty of Science, Technology, Engineering and Mathematics, INTI International University, Putra Nilai, 71800,
Negeri Sembilan, Malaysia

ABSTRACT

In the present study, thin films have been successfully prepared by using various deposition methods. The electrical behaviors of obtained thin films were investigated by using Hall Effect measurement and Van Der Pauw technique as reported by many researchers. The experiment results show that the electrical resistivity reduces with increase in temperature and film thickness as well.

Keywords: Electrical properties, film thickness, resistivity, deposition

INTRODUCTION

Thin films have been prepared using various deposition methods. It is important and need to understand the electrical properties, magnetic properties [1-4], optical properties [5-13], morphological [14-24], structural [25-34] and compositional properties [35-38] of obtained films in order to improve and produce high quality products. Over the past decade, such work has mainly focused on binary films, and ternary films. Recently, there have been few reports on the synthesis of quaternary thin films. Generally, X-ray diffraction technique and energy dispersive X-ray analysis method were used to study the structure and composition of obtained films. Meanwhile, scanning electron microscopy and atomic force microscopy were employed to analysis the morphology of films. Lastly, UV-Visible spectrophotometer was selected to investigate the optical behavior of films.

In this work, thin films were synthesized by using different deposition methods. The obtained films were characterized in terms of their electrical properties.

Literature survey

Naglaa and Masaya, 2005 [39] have reported the deposition of zinc sulfide films on glass substrate using pulsed electrochemical deposition. They proposed that the ZnS films were grown from aqueous solutions containing sodium thiosulphate and zinc sulphate with two different compositions. They conclude that the resistivity ($10^4 \Omega\text{cm}$) of the as-deposited thin films (prepared from the zinc sulphate rich solution and sodium thiosulphate rich solution) is lower than the resistivity of the annealed films ($10^5 \Omega\text{cm}$). It could be attributed to the existence of the zinc in the films, which provides the as-deposited film in gray colour. Additionally, this metal zinc can play a role as a shunt path in the electrical measurements and finally, reduce the resistivity. Low resistivity will give high power factor, higher fill factor and high efficiency. As a result, this behavior makes the films very promising for solar cell applications.

The influence of zinc doping on the electrical properties of lead sulphide films have been studied by Baligh et al., 2015 [40]. The Hall Effect measurements indicate that the value of resistivity of films was affected by the zinc doping in an increasingly proportional manner, whereas from 0.28 to 691.1 Ωcm when increasing zinc doping concentration from 0 to 2 %. It can be attributed to reduce in crystallite size which will increase the grain boundary.

The effect of Sb doping on the properties of PbS films has been investigated by Kumar et al., 2014 [41]. They describe that the solutions were prepared in triple distilled water and the pH was maintained at 12.56. In the electrical characterization, resistance of the films was investigated by the slope of the I-V curves. They point out that the resistivity was observed to be increased with reducing the Sb doping in PbS. The obtained resistivity values have been found to be in the range $1.29 - 3.7 \times 10^6 \Omega\text{cm}$.

Al doped PbS films were synthesized on soda lime glass substrate using simple chemical bath deposition technique as proposed by Preetha and Remadevi, 2012 [42]. Resistivity measurements were performed in their research and electric contacts were given using silver electrodes. Generally, they figure out that reduce in resistivity of bulk compounds if compared to the un-doped compounds.

Khomane, 2013 [43] proposed preparation of $\text{CdS}_{0.5}\text{Se}_{0.5}$ films by an inexpensive chemical bath deposition method. The films were deposited onto non-conducting glass substrate at bath temperature of 70°C . The obtained films were yellowish orange in colour. The electrical resistivity reduces with increase in temperature from 300 to 500 K, indicating an increase in the crystallite size. Finally, they conclude that the electrical specific resistivity was estimated about $10^6 \Omega\text{cm}$.

Spray pyrolysis was used to prepare CuS films as developed by Nho et al., 2012 [44]. They claim that this deposition technique could prepare large-area films without post deposition annealing and additives. The CuS films were synthesized from solutions of $(\text{NH}_2)_2\text{CS}$ and CuCl_2 mixed at ratios of 3:2, 4:1 and 5:1 on glass substrates in the temperature range of 160 to 240°C . The electrical investigation show that for all ratios of starting materials, the low resistivity was stable obtained at temperatures from $170 - 220^\circ\text{C}$. However, high resistivity could be seen for the sample prepared at 240°C .

Cu-doped PbS films were prepared on glass substrates using chemical bath deposition method as reported by Zheng et al., 2016 [45]. The substrate was dipped vertically into bath containing $\text{Pb}(\text{CH}_3\text{COOH})_2$, NaOH and $\text{SC}(\text{NH}_2)_2$. It is observed that resistivity of the films reduce from $16 \Omega\text{cm}$ to $0.15 \Omega\text{cm}$ as the copper concentration increases from 0 to 6.3 %. This phenomenon could be explained by the fact that an increase in the carrier concentration and larger crystallite sizes. However, resistivity increases from $0.15 \Omega\text{cm}$ to $1.09 \Omega\text{cm}$ for further increase in copper concentration from 6.3% to 7.9%. In this case, they explain that an increase in resistivity is because of increase in the grain boundaries, lattice defects and discontinuities.

Chemical spray deposition technique was employed to prepare $\text{Cd}_{1-x}\text{Fe}_x\text{Se}$ films onto glass substrate as proposed by Ashok and Syed, 2015 [46]. During the experiment, ferric chloride, cadmium chloride and selenium powder were used as the precursors. They observe that the electrical resistivity of these films is of the order of $10^6 \Omega\text{cm}$ and indicates variation depending on "x" value. On the other hand, they point out that the electrical resistivity for CdSe and FeSe films are $10.7 \times 10^6 \Omega\text{cm}$ and $1.23 \times 10^6 \Omega\text{cm}$, respectively.

Chemical bath deposition method was used to produce Cu_2S thin films in the presence of precursors such as copper chloride, thiourea and ammonia as described by Shinde et al., 2012 [47]. Deposition was carried in alkaline conditions (pH between 8 to 9) at room temperature. They conclude that the electrical resistivity reduces from 6.4×10^{-2} to $8.9 \times 10^{-3} \Omega\text{cm}$ when the thickness increases from 130 to 400 nm. They explain that when film thickness increases, the islands of aggregated particles transform into continuous bands. On the other hand, Soliman et al., 2015 [48] also produce similar results. They claim that electrical resistivity decreases with increasing thermal evaporated Cu_2S film thickness from 113 to 453 nm.

On the other hand, quaternary thin films have been prepared by many researchers. Kumar et al., 2009 [49] have reported that $\text{Cu}_2\text{ZnSnS}_4$ films were synthesized using spray pyrolysis method on soda lime glass substrate. The electrical resistivity was investigated using Van Der Pauw technique for the films deposited under various substrate temperatures in the range of 563 to 723 K. They conclude that the resistivity was found to vary from 0.2 to $2 \Omega\text{cm}$. Mkawi et al., 2013 [50] suggest that the electrical property depend on the copper concentration in the precursor. They can observe that the resistivity of electrodeposited $\text{Cu}_2\text{ZnSnS}_4$ films decreased from 12.5 to $6.98 \Omega\text{cm}$ with increasing the copper content from 0.01 M to 0.04 M.

CONCLUSION

The electrical resistivity of the films was successfully measured by using different tools. The SI unit of resistivity is the ohm.meter. Generally, low resistivity means a particular film that readily allows the flow of electric current. The

obtained results indicate that there are many conditions such as film thickness, crystallinity of sample, and morphology of films can affect electrical resistivity properties.

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